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FIG.11

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**PRIOR ART** 

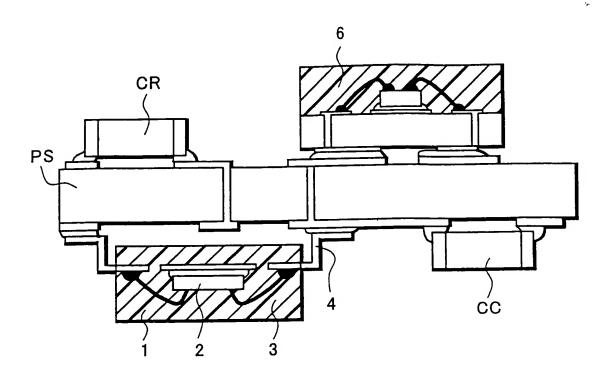
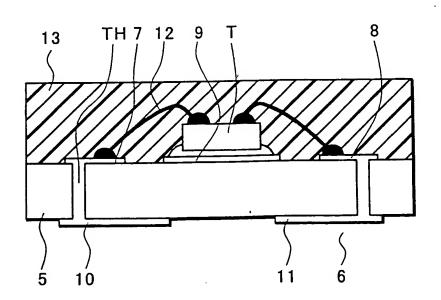


FIG.12

## **PRIOR ART**





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FIG.13A

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## **PRIOR ART**

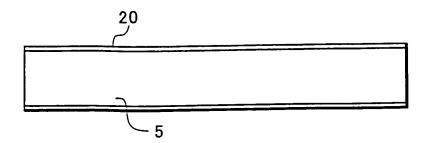
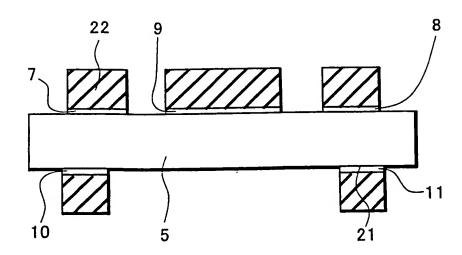


FIG.13B

## **PRIOR ART**



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**FIG.14** 

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## **PRIOR ART**

